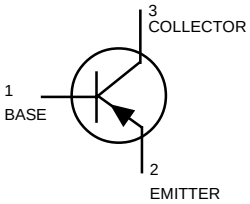
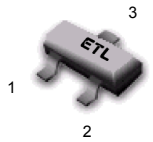
ETL SEMICONDUCTOR

High Voltage Transistor

PNP Silicon

MMBTA92LT1
MMBTA93LT1





CASE 318–08, STYLE 6
SOT–23 (TO–236AB)

MAXIMUM RATINGS

Rating	Symbol	Value MMBTA92	Value MMBTA93	Unit
Collector–Emitter Voltage	V_{CEO}	–300	–200	Vdc
Collector–Base Voltage	V_{CBO}	–300	–200	Vdc
Emitter–Base Voltage	V_{EBO}	–5.0		Vdc
Collector Current — Continuous	I_C	–500		mAdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR– 5 Board, (1) T_A = 25°C	P_D	225	mW
Derate above 25°C		1.8	mW/°C
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	556	°C/W
Total Device Dissipation Alumina Substrate, (2) T_A = 25°C	P_D	300	mW
Derate above 25°C		2.4	mW/°C
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	417	°C/W
Junction and Storage Temperature	T_J , T_{stg}	–55 to +150	°C

DEVICE MARKING

MMBTA92LT1 = 2D, MMBTA93LT1 = 2E

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted.)

Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTICS					
Collector–Emitter Breakdown Voltage(3)		$V_{(BR)CEO}$			Vdc
(I_C = –1.0 mAdc, I_B = 0)	MMBTA92		–300	—	
	MMBTA93		–200	—	
Collector–Emitter Breakdown Voltage		$V_{(BR)CBO}$			Vdc
(I_C = –100 µAdc, I_E = 0)	MMBTA92		–300	—	
	MMBTA93		–200	—	
Emitter–Base Breakdown Voltage (I_E = –100 µAdc, I_C = 0)		$V_{(BR)EBO}$	–5.0	—	Vdc
Collector Cutoff Current		I_{CBO}			nAdc
(V_{CB} = –200Vdc, I_E = 0)	MMBTA92		—	–0.25	
(V_{CB} = –160Vdc, I_E = 0)	MMBTA93		—	–0.25	
Collector Cutoff Current (V_{CB} = –3.0Vdc, I_C = 0)		I_{EBO}	—	–0.1	µAdc

1. FR–5 = 1.0 x 0.75 x 0.062 in.
2. Alumina = 0.4 x 0.3 x 0.024 in. 99.5% alumina.
3. Pulse Test: Pulse Width ≤ 300 µs, Duty Cycle ≤ 2.0%.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted) (Continued)

Characteristic		Symbol	Min	Max	Unit
ON CHARACTERISTICS (3)					
DC Current Gain		h_{FE}			—
(I_C =–1.0mAdc, V_{CE} = –10 Vdc)	Both Types		25	—	
(I_C = –10 mAdc, V_{CE} = –10Vdc)	Both Types		40	—	
(I_C = –30mAdc, V_{CE} =–10 Vdc)	MMBTA92		25	—	
	MMBTA93		25	—	
Collector–Emitter Saturation Voltage		$V_{CE(sat)}$			Vdc
(I_C = –20mAdc, I_B = –2.0 mAdc)	MMBTA92		—	–0.5	
	MMBTA93		—	–0.5	
Base–Emitter Saturation Voltage (I_C = –20mAdc, I_B = –2.0 mAdc)		$V_{BE(sat)}$	—	–0.9	Vdc
SMALL–SIGNAL CHARACTERISTICS					
Current–Gain — Bandwidth Product(3),(4) (I_C = –10mAdc, V_{CE}= –20Vdc, f = 100MHz)		f_T	50	—	MHz
Collector – Base Capacitance		C_{cb}			pF
(V_{CB} = –20 Vdc, I_E = 0, f = 1.0 MHz)	MMBTA92		—	6.0	
	MMBTA93		—	8.0	

3. Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2.0%.

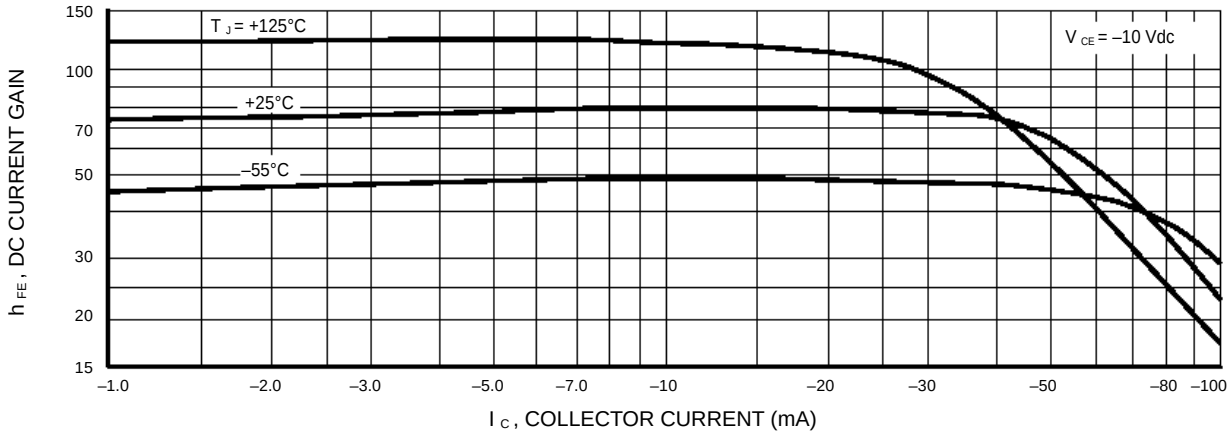


Figure 1. DC Current Gain

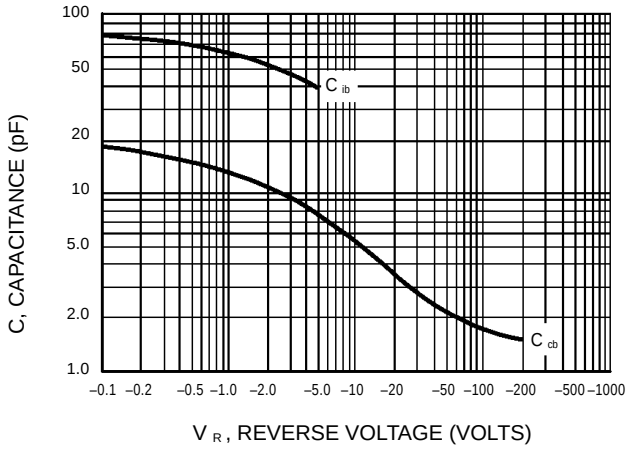


Figure 2. Capacitances

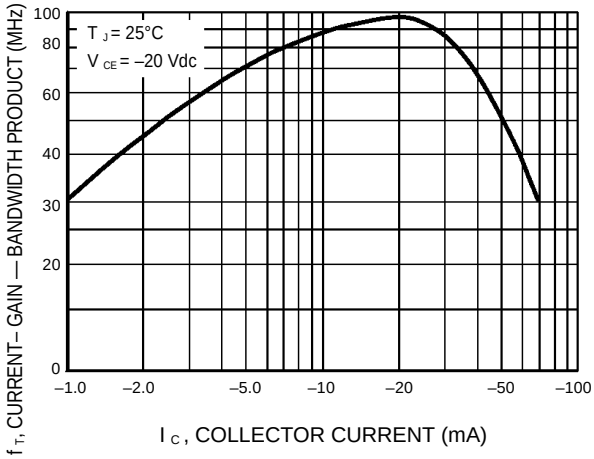


Figure 3. Current–Gain — Bandwidth Product

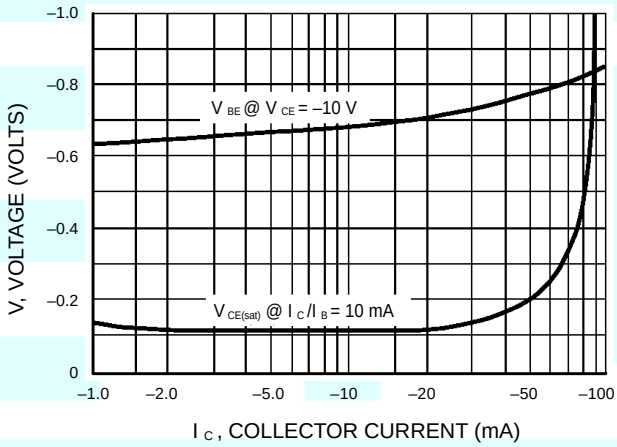


Figure 4. “On” Voltages