

GLLM317L

100mA ADJUSTABLE VOLTAGE REGULATOR

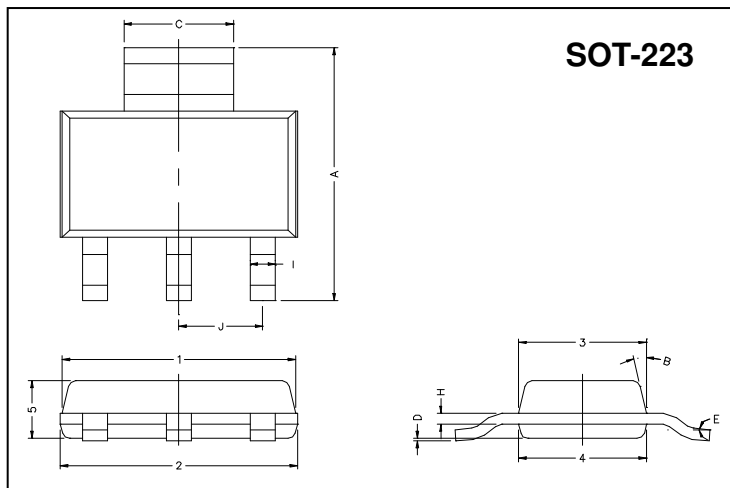
Description

The GLLM317L is a monolithic integral circuit, designed for use as positive adjustable voltage regulator. It is designed to supply unit 100mA of load current with an output voltage adjustable over a 1.25V to 37V range.

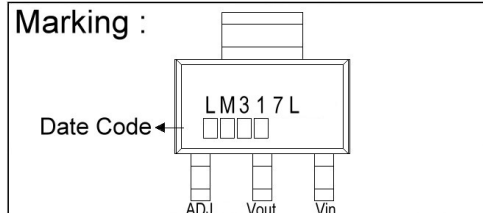
Features

- Output voltage adjustable from 1.25V to 37V
- Output current in excess of 100mA
- Thermal overload protection
- Short circuit protection
- Output transistor save area compensation
- Floating operation for high voltage applications

Package Dimensions

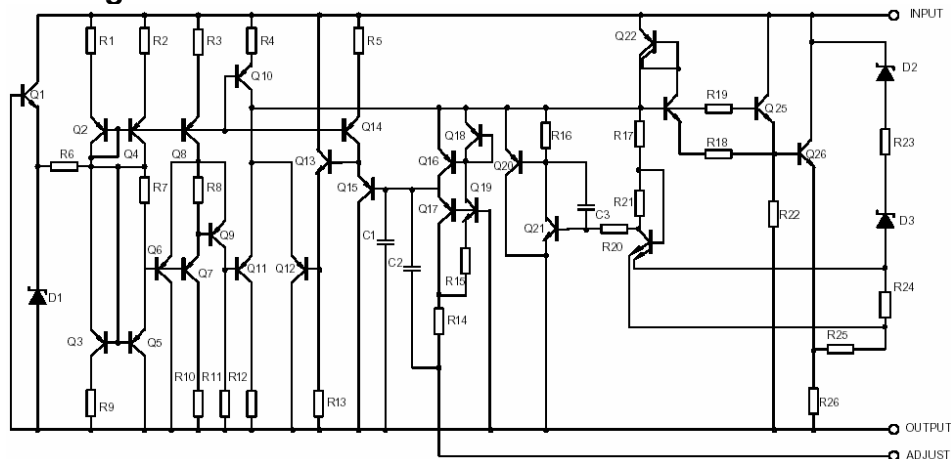


Marking :

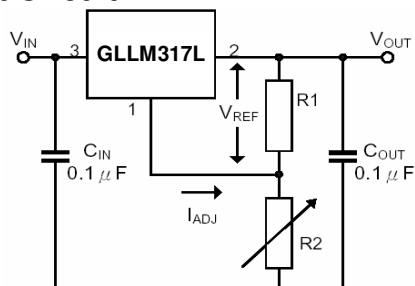


REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	6.70	7.30	B	13° TYP.	
C	2.90	3.10	J	2.30 REF.	
D	0.02	0.10	1	6.30	6.70
E	0°	10°	2	6.30	6.70
I	0.60	0.80	3	3.30	3.70
H	0.25	0.35	4	3.30	3.70
			5	1.40	1.80

Block Diagram



Test Circuit



Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Ratings	Unit
Input-Output Voltage Difference	V _{IN} - V _{OUT}	40	V
Power Dissipation	P _D	1	W
Junction Temperature	T _J	+125	°C
Operating Junction Temperature	T _{OPR}	0 ~ +70	°C
Storage Temperature Range	T _{STR}	-40 ~ +150	°C

Note 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. The device is guaranteed to meet performance specification within 0°C ~ 70°C operating temperature range and assured by design from -20°C ~ 85 °C.

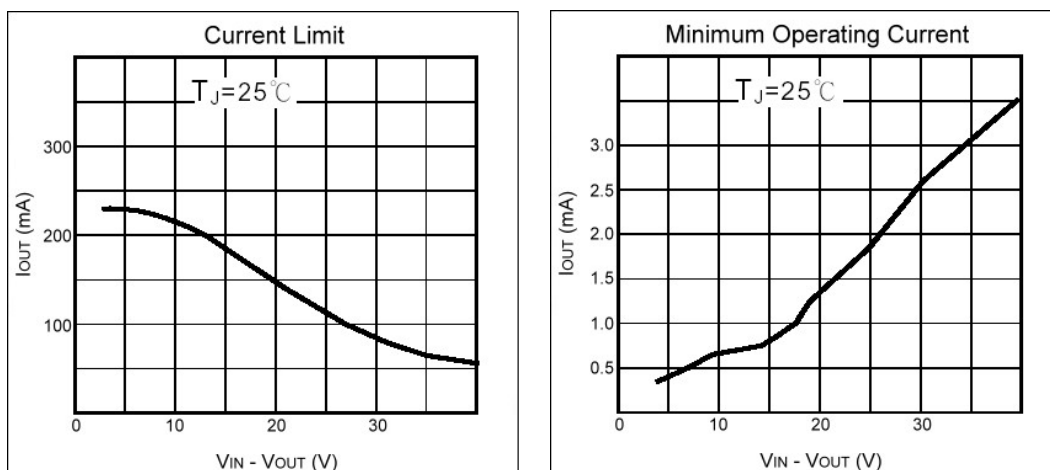
Electrical Characteristics

(V_{IN} - V_{OUT}=5V, 0°C ≤ T_J ≤ 125°C, I_O=40mA, unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	TYP	Max	Unit
Line Regulation	REGLINE	V _{IN} - V _{OUT} =3~40V I _O <20mA	T _J =25°C	-	0.01	0.04 %/V
			0°C ≤ T _J ≤ 125°C	-	0.02	0.07 %/V
Load Regulation	REGLOAD	I _O =5~100mA V _{OUT} ≤ 5V	T _J =25°C	-	5	25 mV
			0°C ≤ T _J ≤ 125°C	-	20	70 mV
		I _O =5~100mA V _{OUT} ≥ 5V	T _J =25°C	-	0.1	0.5 %/V _{OUT}
			0°C ≤ T _J ≤ 125°C	-	0.3	1.5 %/V _{OUT}
Adjustable Pin Current	I _{ADJ}		-	50	100	μA
Adjustable Pin Current Change	ΔI _{ADJ}	V _{IN} - V _{OUT} =3~40V I _O =5~100mA, P _D < 625mW	-	0.2	5	μA
Reference Voltage	V _{REF}	V _{IN} - V _{OUT} =3~40V I _O =5~100mA, P _D < 625mW	1.2	1.25	1.3	V
Output Voltage Temperature Stability	ΔV _{OUT} /V _{OUT}		-	0.7	-	%
Minimum Load Current	I _{LOAD} (Min)	V _{IN} - V _{OUT} =40V	-	3.5	5	mA
Maximum Output Current	I _{OUT} (Max)	V _{IN} - V _{OUT} =3~13V	100	200	-	mA
		V _{IN} - V _{OUT} =40V	25	50	-	mA
Output Noise Voltage (% of V _{OUT})	e _N	f=10Hz ~ 10KHz, T _J =25°C	-	0.003	-	%/V _{OUT}
Ripple Rejection	RR	T _J =25°C, f=120Hz	C _{ADJ} =0	-	65	- dB
			C _{ADJ} =10μF	66	80	- dB

Note: C_{ADJ} is connected between Adjust pin and Ground.

Characteristics Curve



Application Information and Circuits

The GLLM317L provides an internal reference voltage of 1.25V between the output and adjustments terminals. This is used to set a constant current flow across an external resistor driver, giving an output voltage V_{OUT} of:

$$V_{OUT} = V_{REF} * (1 + R_2 / R_1) + I_{ADJ} * R_2$$

The device is designed to minimize the term I_{ADJ} (100uA max) and to maintain it very constant with line and load changes. Usually, the error term $I_{ADJ} * R_2$ can be neglected. To obtain the previous requirement, all the regulator quiescent current is returned to the output terminal, imposing a minimum load current condition. If the load is insufficient, the output voltage will rise. The GLLM317L is a floating regulator, input-output differential voltage, supplies of very high voltage with respect to ground can be regulated as long as the maximum input-output differential is not exceeded. Furthermore, programmable regulators are easily obtainable and, by connecting a fixed resistor between the adjustment and output, the device can be used as a precision current regulator.

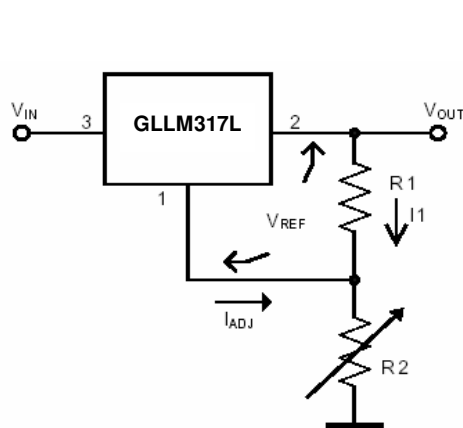


Fig 1. Basic Adjustable Regulator

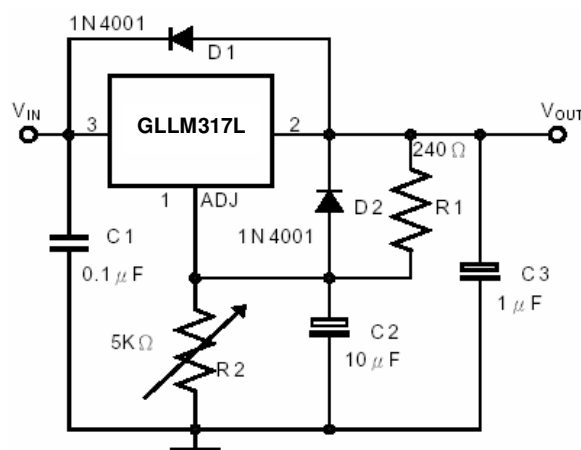


Fig 2. Voltage Regulator with Protection Diodes

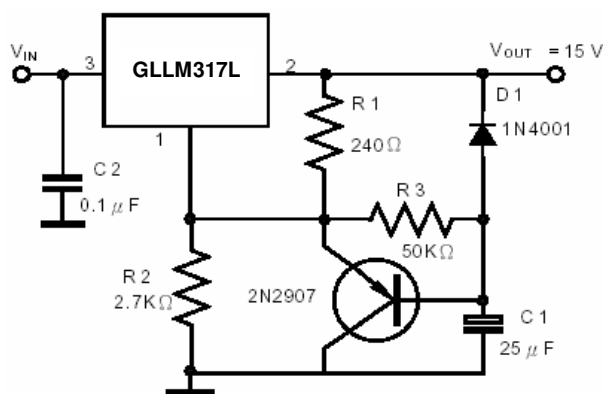


Fig 3. Slow Turn-On 15V Regulator

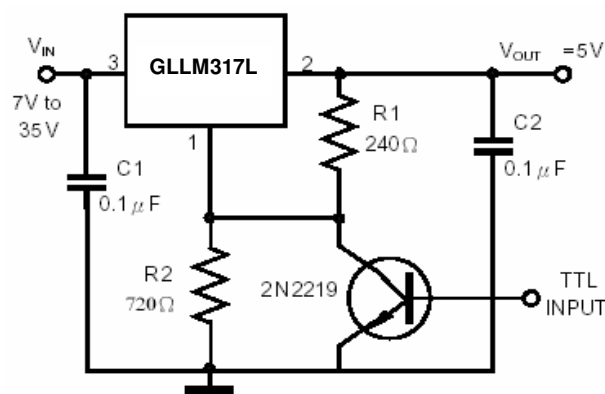


Fig 4. 5V Electronic Shut-Down Regulator

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