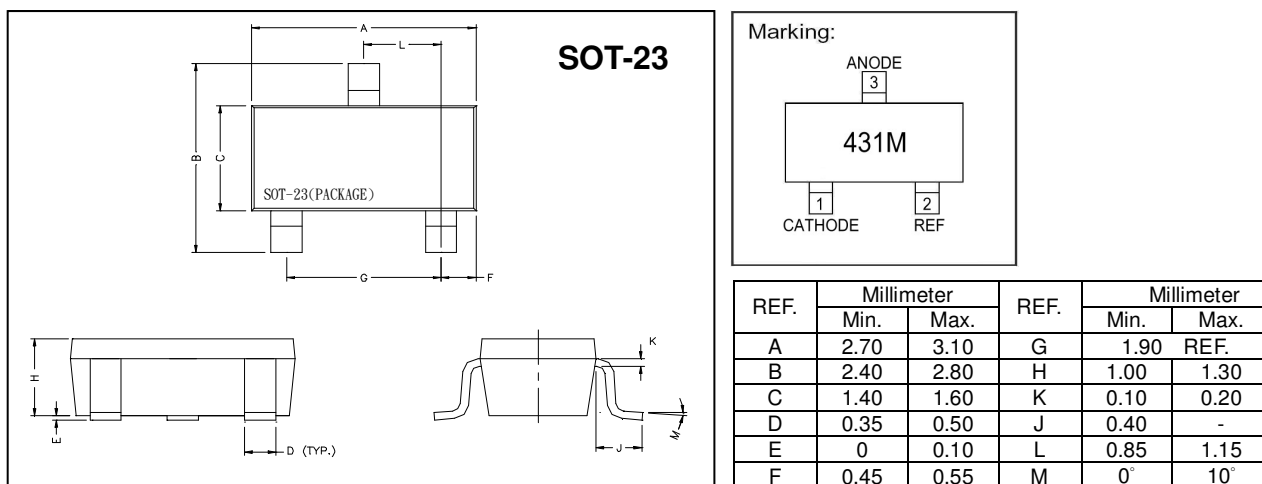


G431M Adjustable Shunt Regulator

Description

The G431M series are three-terminal adjustable regulators with guaranteed thermal stability over applicable temperature ranges. The output voltage may be set to any value between VREF (approximately 2.495v) and 36v with two external resistors. It provides very wide applications, including shunt regulator, series regulator, switching regulator, voltage reference and others.

Package Dimensions



Absolute Maximum Ratings at Ta = 25°C

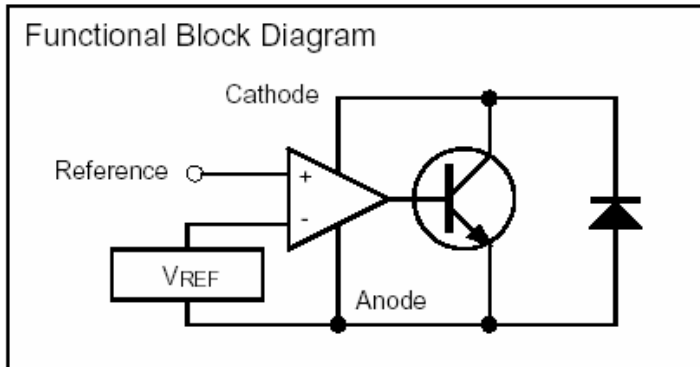
Parameter	Symbol	Ratings	Unit
Junction Temperature	Tj	+150	°C
Storage Temperature	Tstg	-65 ~ +150	°C
Cathode Voltage	VKA	37	V
Cathode Current Range(Continuous)	IkA	-100~+150	mA
Reference Input Current Range	IREF	-0.05~+10	mA
Total Power Dissipation	PD	225	mW

Characteristics at Ta = 25°C

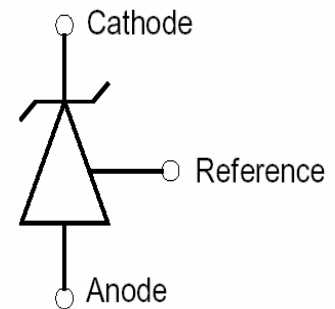
Parameter	Symbol	Min	Typ.	Max.	Unit	Test Conditions
Cathode Voltage	VKA	VREF	-	36	V	
Cathode Current	IkA	1	-	100	mA	
Reference Input Voltage	G431MC G431MB G431MA	2.445 2.470 2.483	2.495 2.495 2.495	2.545 2.520 2.507	V	VKA= VREF, Ik=10mA
Deviation of reference Input Voltage Over temperature(note)	$\Delta V_{REF}/\Delta T$	-	4.5	17	mV	VKA=VREF, Ik=10mA Tmin ≤ Ta ≤ Tmax
Ratio of Change in Reference Input Voltage to the Change in Cathode Voltage	$\Delta V_{REF}/\Delta V_{KA}$	-	-1.0 -0.5	-2.7 -2.0	mV/V	Ik=10mA $\Delta V_{KA}=10V \sim V_{REF}$ $\Delta V_{KA}=36V \sim 10V$
Reference Input Current	IREF	-	1.5	4	uA	Ik=10mA, R1=10KΩ, R2=∞
Deviation of reference Input Current Over Full Temperature Range	$\Delta I_{REF}/\Delta T$	-	0.4	1.2	uA	Ik=10mA, R1=10KΩ, R2=∞ TA=Full Temperature
Minimum Cathode Current for Regulation	IkA(min)	-	0.45	1.0	mA	VKA=VREF
Off-State Cathode Current	IkA(off)	-	0.05	1.0	uA	VKA=36V, VREF=0
Dynamic Impedance	ZKA	-	0.15	0.5	Ω	VKA=VREF, Ik=1 to 100mA, F≤1.0KHz

Note: Tmin= 0°C, Tmax=+70°C

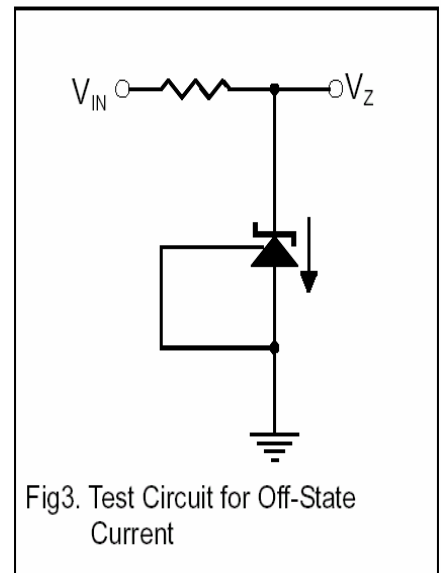
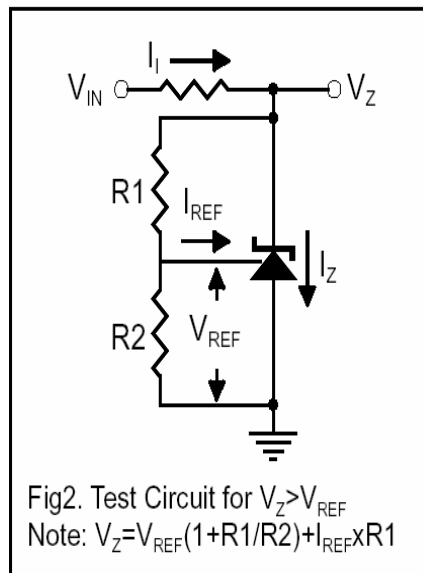
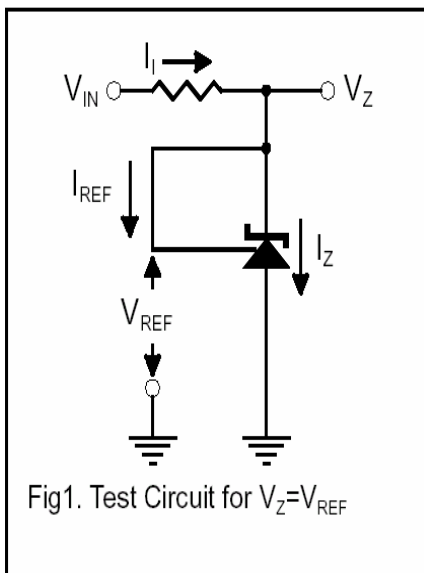
Functional Block Diagram & Symbol



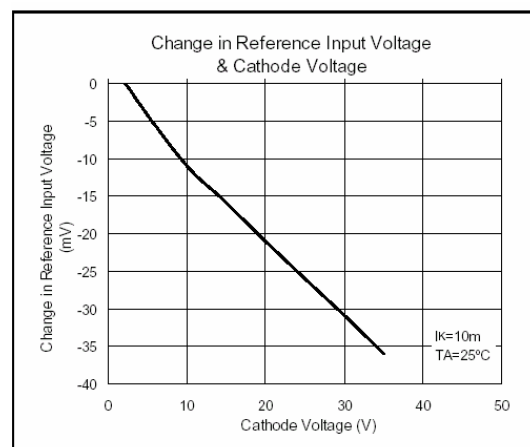
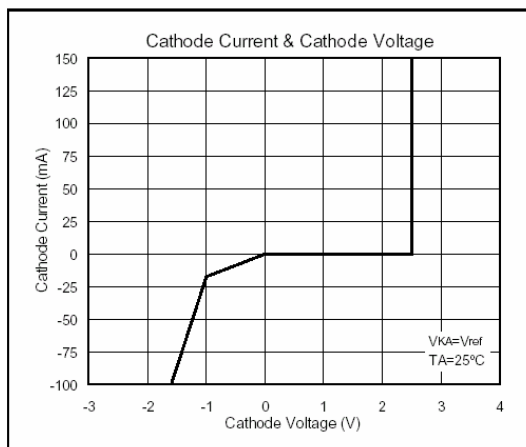
Symbol:

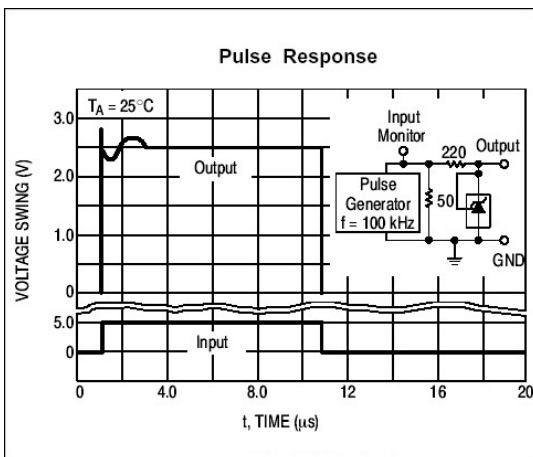
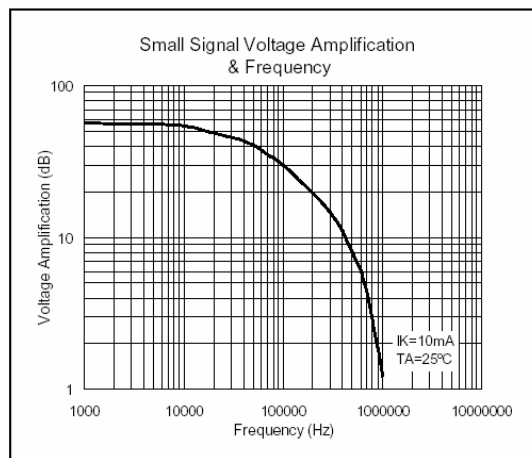
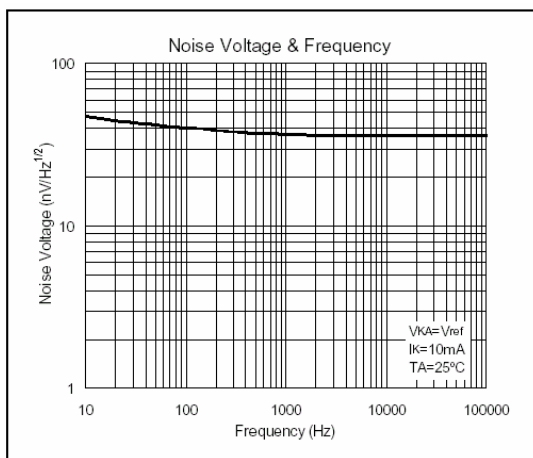
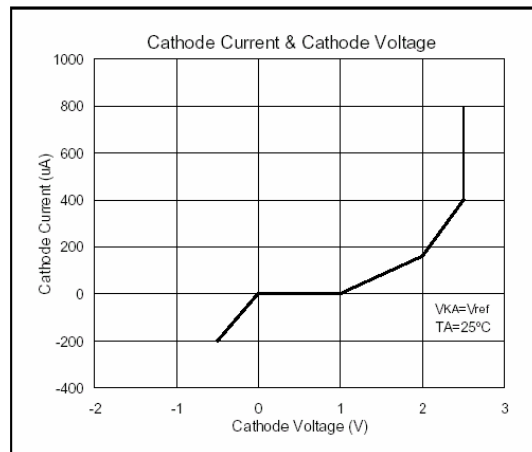
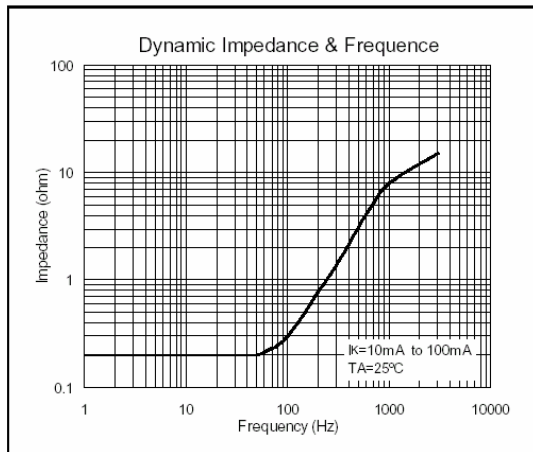


Test Circuits



Characteristics Curve





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Head Office And Factory:

- **Taiwan:** No. 17-1 Tatung Rd. Fu Kou Hsin-Chu Industrial Park, Hsin-Chu, Taiwan, R. O. C.
TEL : 886-3-597-7061 FAX : 886-3-597-9220, 597-0785
- **China:** (201203) No.255, Jang-Jiang Tsai-Lueng RD. , Pu-Dung-Hsin District, Shang-Hai City, China
TEL : 86-21-5895-7671 ~ 4 FAX : 86-21-38950165