

GTL432

Adjustable Shunt Regulator

Description

The GTL432 series are three-terminal adjustable precision shunt regulators with guaranteed stable temperature over the applicable extended commercial temperature range. The output voltage may be set at any level greater than 1.24V (V_{REF}) up to 20V merely by selecting two external resistors that act as a voltage divider network. These devices have a typical output impedance of 0.2Ω. Active output circuitry provides very sharp turn-on characteristics, making these devices excellent improved replacements for Zener diodes in many applications.

The precise $\pm 2\%$ reference voltage tolerance of GTL432 make it possible in many applications to avoid the use of a variable resistor, consequently saving cost and eliminating drift and reliability problems associated with it.

Features

*Precision reference voltage

A Rank: 1.24V $\pm 0.5\%$

B Rank: 1.24V $\pm 1\%$

C Rank: 1.24V $\pm 2\%$

*Sink current capability: 200 mA.

*Minimum cathode current for regulation: 150 μ A.

*Equivalent full-range temp coefficient: 30 ppm/°C.

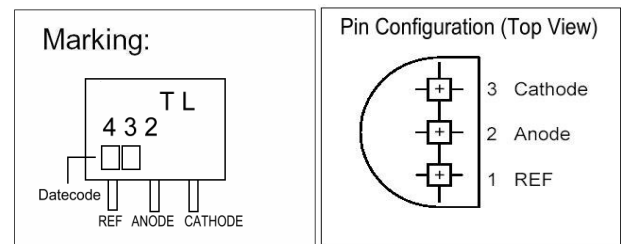
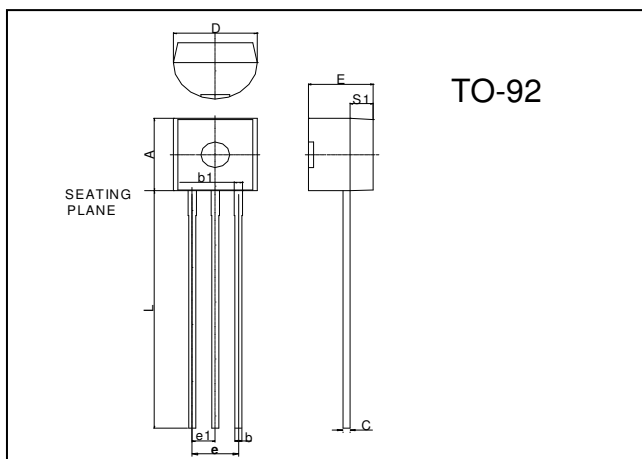
*Fast turn-on Response.

*Low dynamic output impedance: 0.2Ω.

*Programmable output voltage to 20V.

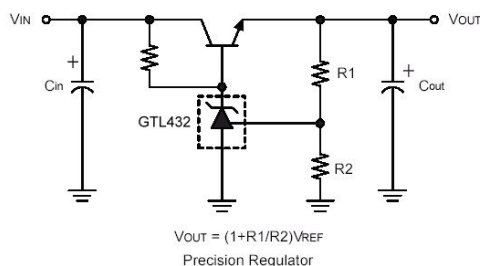
*Low output noise.

Package Dimensions

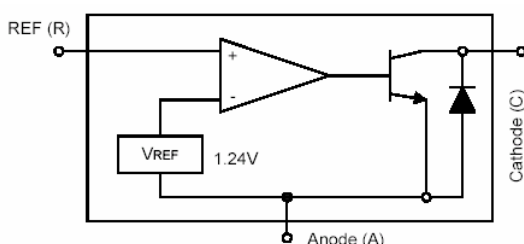


REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	4.45	4.7	D	4.44	4.7
S1	1.02	-	E	3.30	3.81
b	0.36	0.51	L	12.70	-
b1	0.36	0.76	e1	1.150	1.390
C	0.36	0.51	e	2.42	2.66

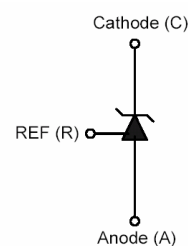
Typical Application Circuit



Block Diagram



Symbol

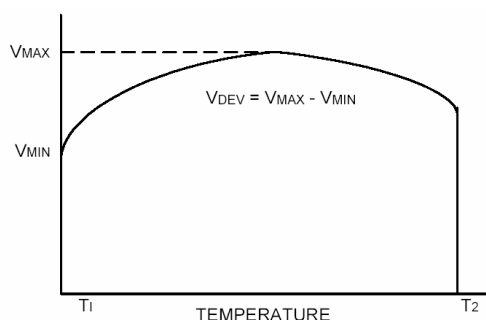


Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Ratings	Unit
Junction Temperature	Tj	+150	°C
Storage Temperature	Tstg	-65 ~ +150	°C
Cathode Voltage	VKA	20	V
Continuous Cathode Current	IKA	-10~+250	mA
Reference Input Current Range	IREF	-0.05~+10	mA
Total Power Dissipation	PD	225	mW

Electrical Characteristics (Ta = 25°C, unless otherwise specified.)

Parameter	Symbol	Min	Typ.	Max.	Unit	Test Conditions
Reference Voltage GTL432C GTL432B GTL432A	VREF	1.215 1.228 1.234	1.24 1.24 1.24	1.265 1.252 1.246	V	VKA= VREF , Ik=10mA (Fig.1)
Deviation of reference Input Voltage Over temperature(note1)	ΔVREF/ΔT	-	3.0	20	mV	VKA= VREF , Ik=10mA Ta=Full range (Fig.1)
Ratio of Change in Reference Input Voltage to the Change in Cathode Voltage	ΔVREF/Δ VKA	-	-1.4	-2.0	mV/V	Ik=10mA (Fig.2) ΔVKa=20V~VREF
Reference Input Current	IREF	-	1.4	3.5	uA	Ik=10mA, R1=10KΩ, R2=∞ (Fig.2)
Deviation of reference Input Current Over Temperature Range	α IREF	-	0.4	1.2	uA	Ik=10mA, R1=10KΩ, R2=∞ Ta=Full range(Fig.2)
Minimum Cathode Current for Regulation	IKa(min)	-	0.15	0.3	mA	VKA=VREF(Fig.1)
Off-State Cathode Current	IKa(off)	-	0.1	1.0	uA	VKA=20V, VREF=0 (Fig.3)
Dynamic Output Impedance (note2)	ZKA	-	0.2	0.5	Ω	VKA=VREF, Ik=1 ~100mA F≤1.0KHz(Fig.1)



Note1. Deviation of reference input voltage, VDEV, is defined as the maximum variation of the reference over the full temperature range.

The average temperature coefficient of the reference input voltage α VREF is defined as:

$$|\alpha V_{REF}| = \frac{\left(\frac{V_{DEV}}{V_{REF}(25^{\circ}C)} \right) \times 10^6}{T_2 - T_1} \dots\dots\dots (ppm/^{\circ}C)$$

Where:

T2 – T1 = full temperature change.

α VREF can be positive or negative depending on whether the slope is positive or negative.

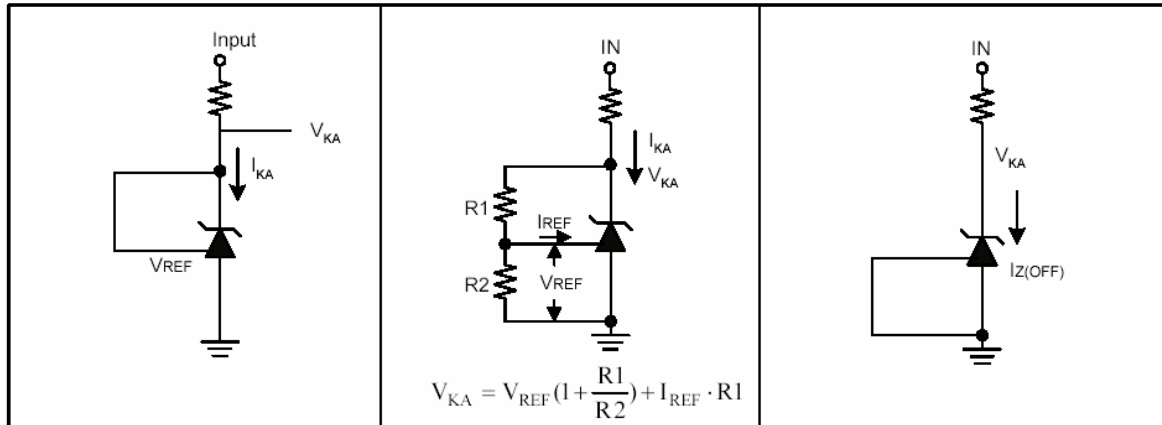
Note2. The dynamic output impedance, RZ, is defined as:

$$|Z_{KA}| = \frac{\Delta V_{KA}}{\Delta I_{KA}}$$

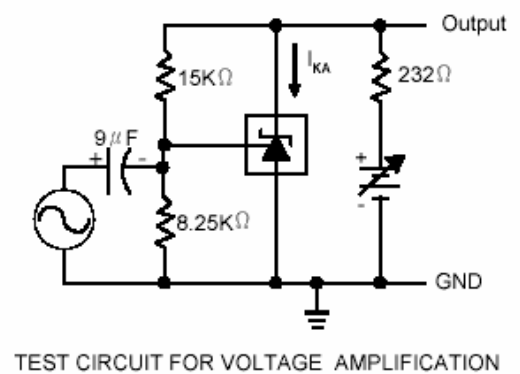
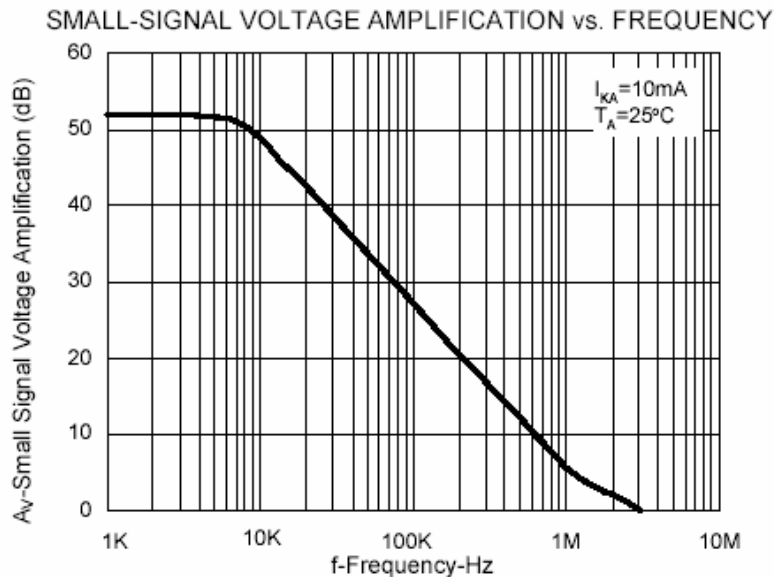
When the device is programmed with two external resistors R1 and R2 (see figure 2.), the dynamic output impedance of the overall circuit, is defined as:

$$|Z_{KA}| = \frac{\Delta V}{\Delta I} > |Z_{KA}| \left(1 + \frac{R1}{R2}\right)$$

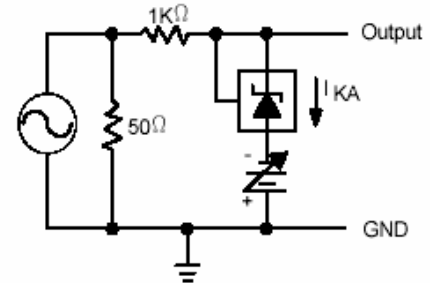
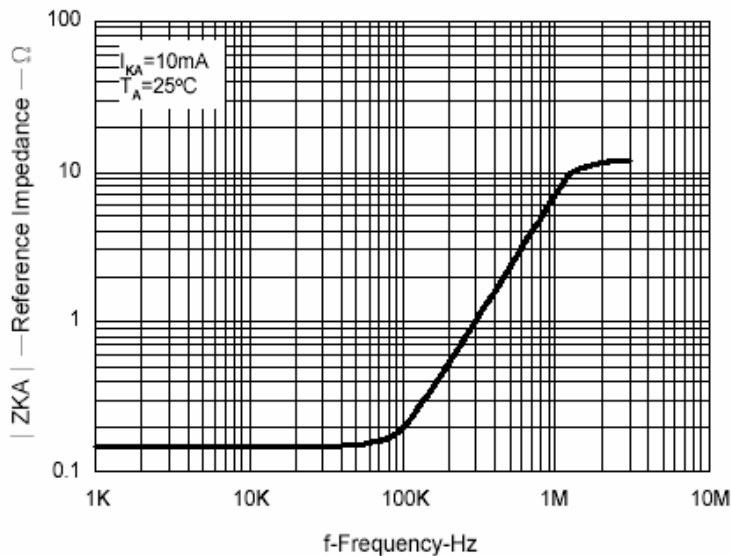
Test Circuits



Typical Performance Characteristics

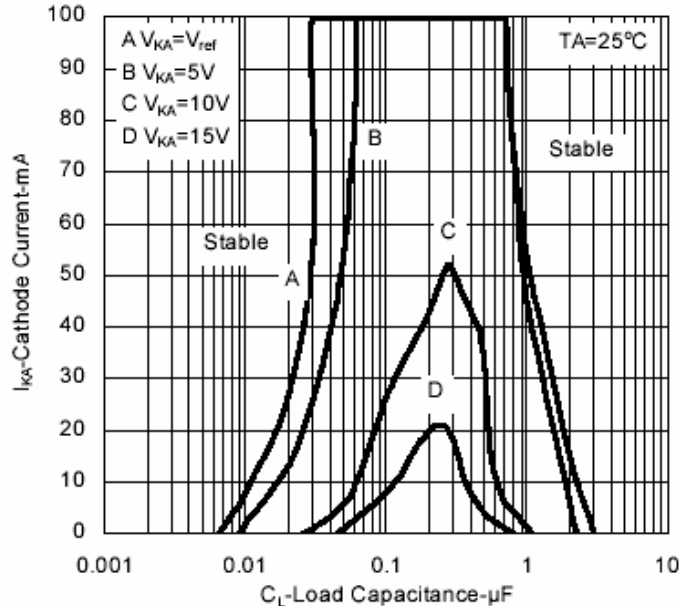


REFERENCE IMPEDANCE vs. FREQUENCY

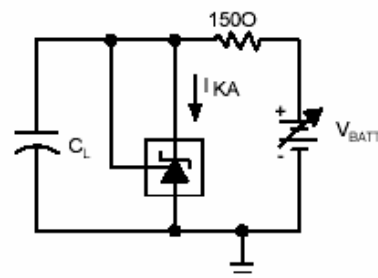


TEST CIRCUIT FOR REFERENCE IMPEDANCE

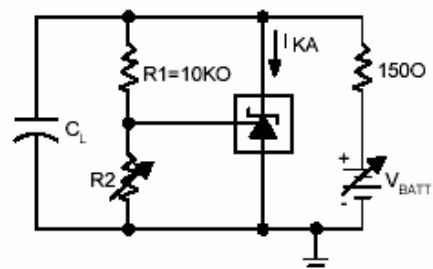
STABILITY BOUNDARY CONDITIONS†



†The areas under the curves represent conditions that may cause the device to oscillate. For curves B, C, and D, R2 and V+ were adjusted to establish the initial V_{KA} and I_{KA} conditions with $C_L=0$. V_{BATT} and C_L were then adjusted to determine the ranges of stability.

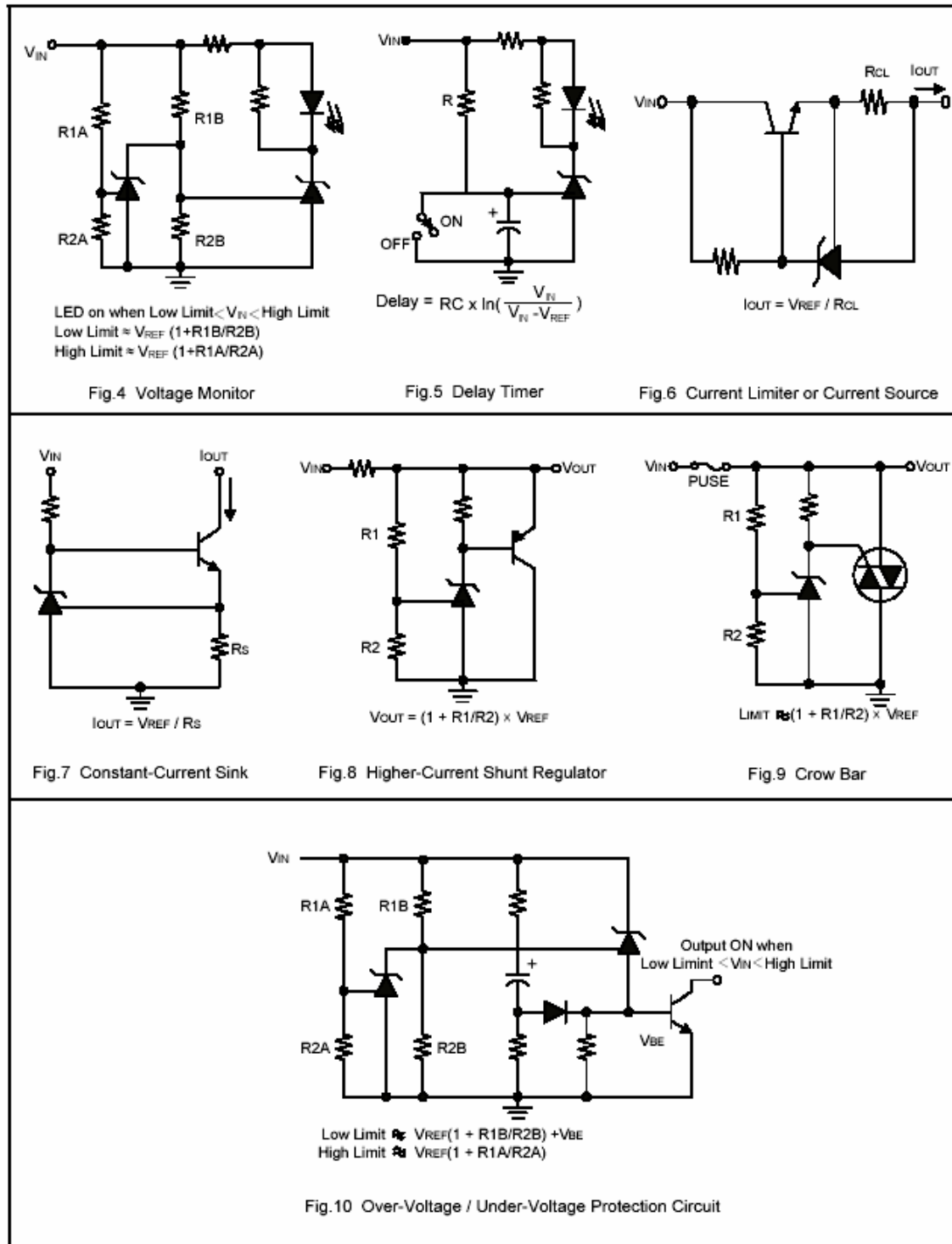


TEST CIRCUIT FOR CURVE A



TEST CIRCUIT FOR CURVE B, C, AND D

Application Examples



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